

FIG. 1 is a schematic diagram of a prior art device.

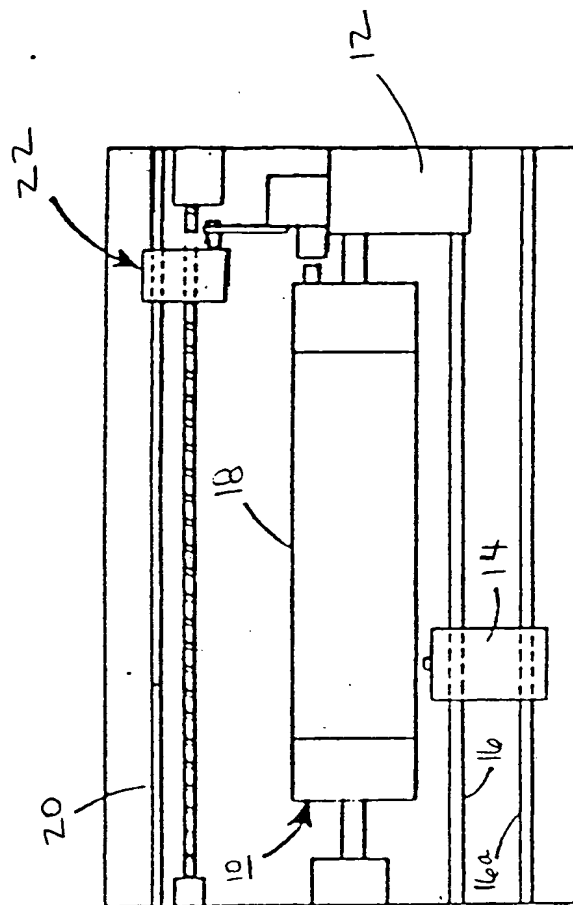


Figure 1
Prior Art

52

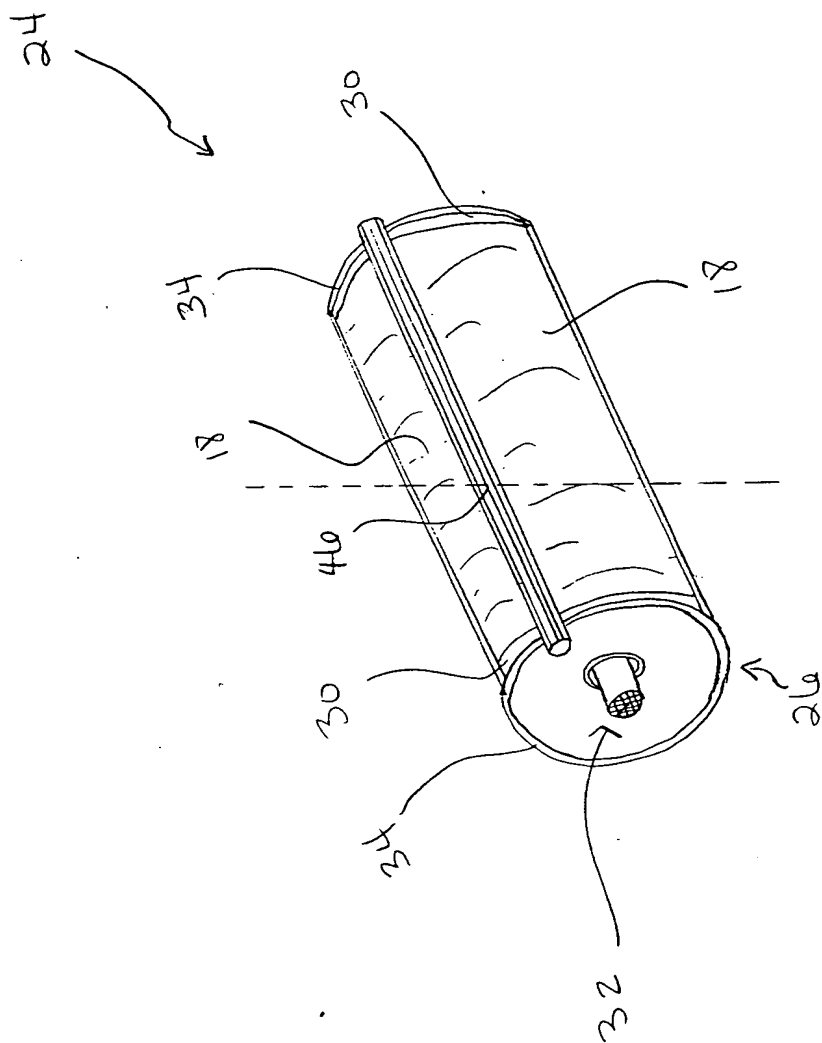


Figure 2

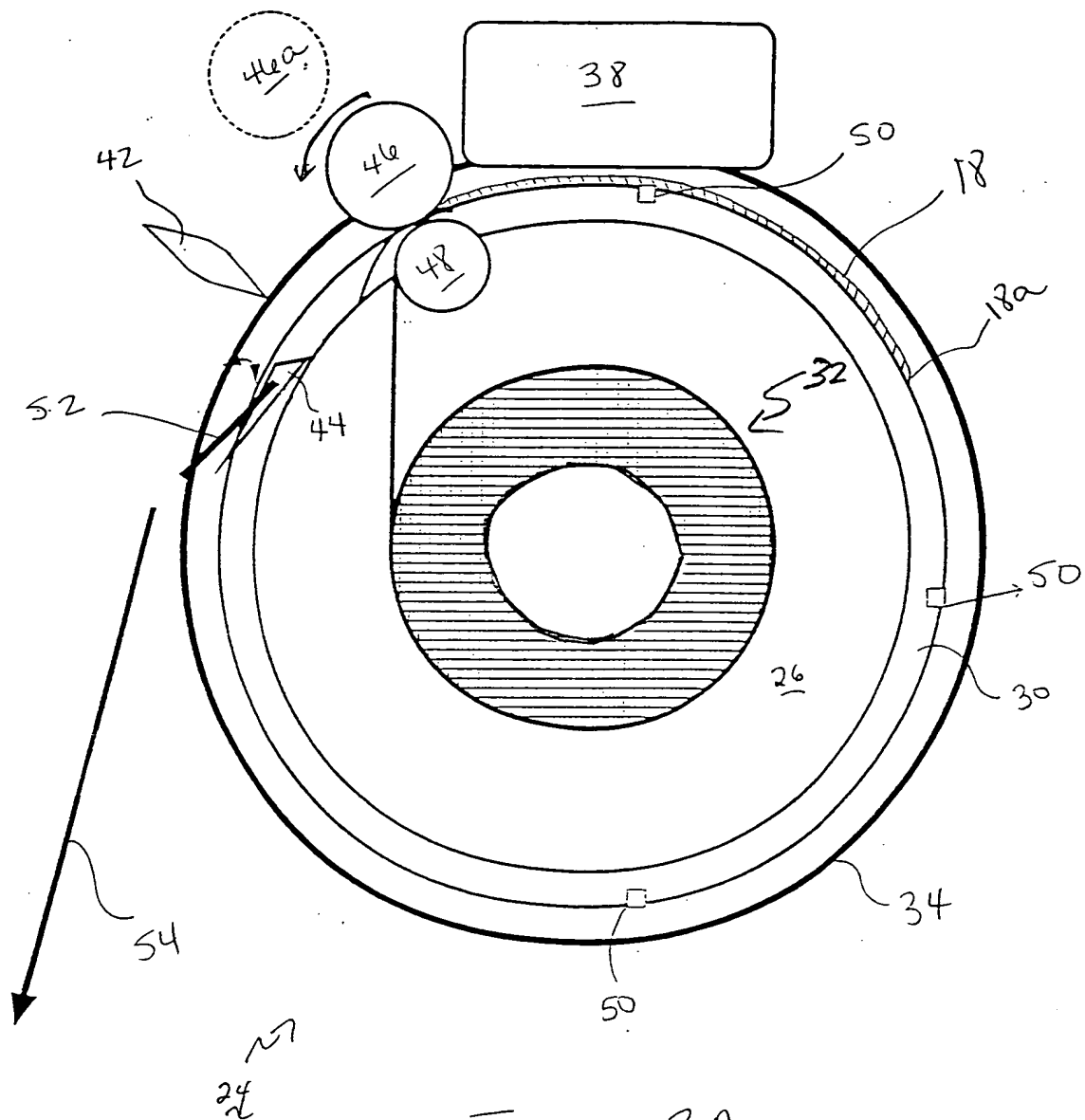


Figure 3A

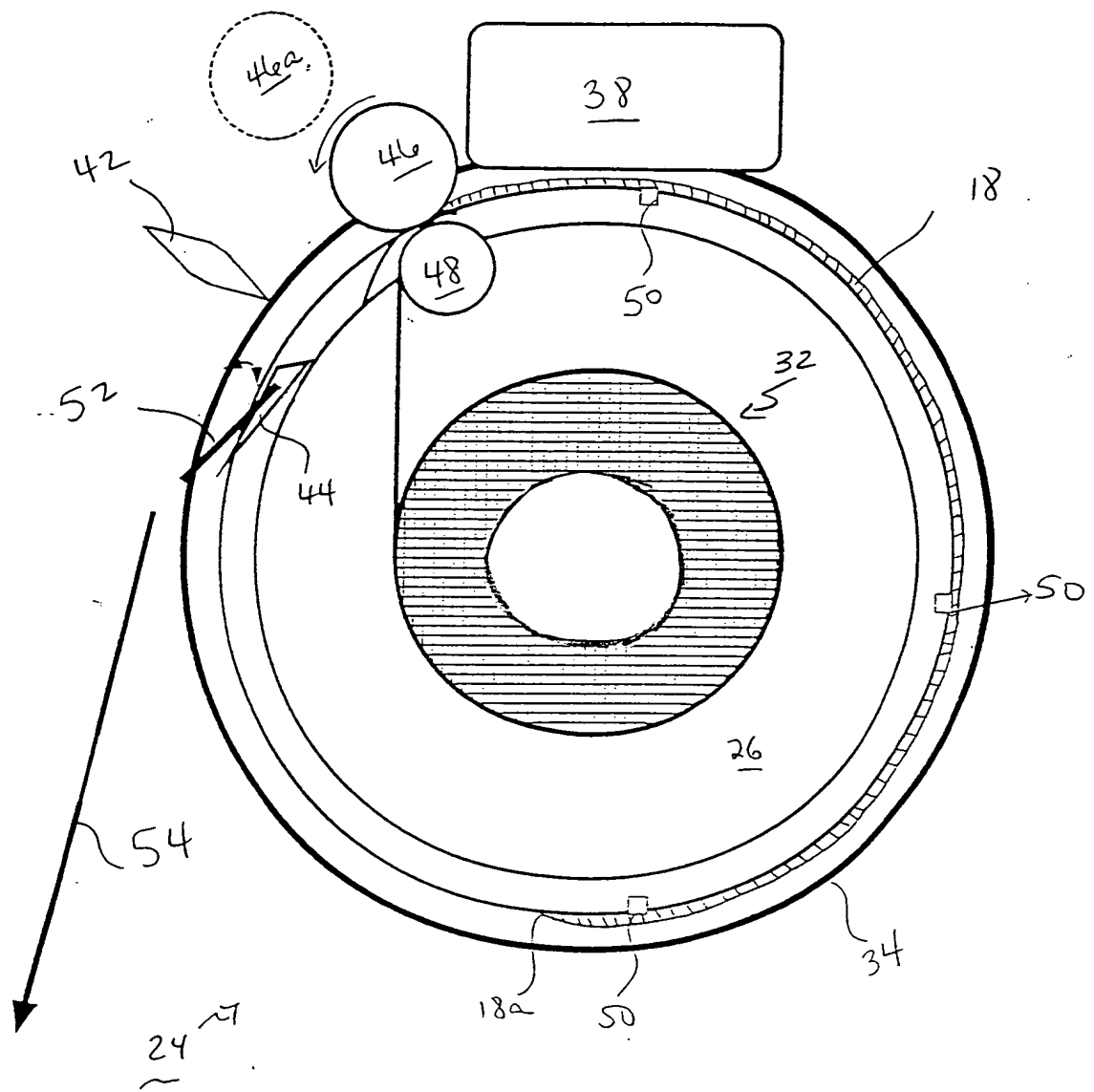


Figure 3B

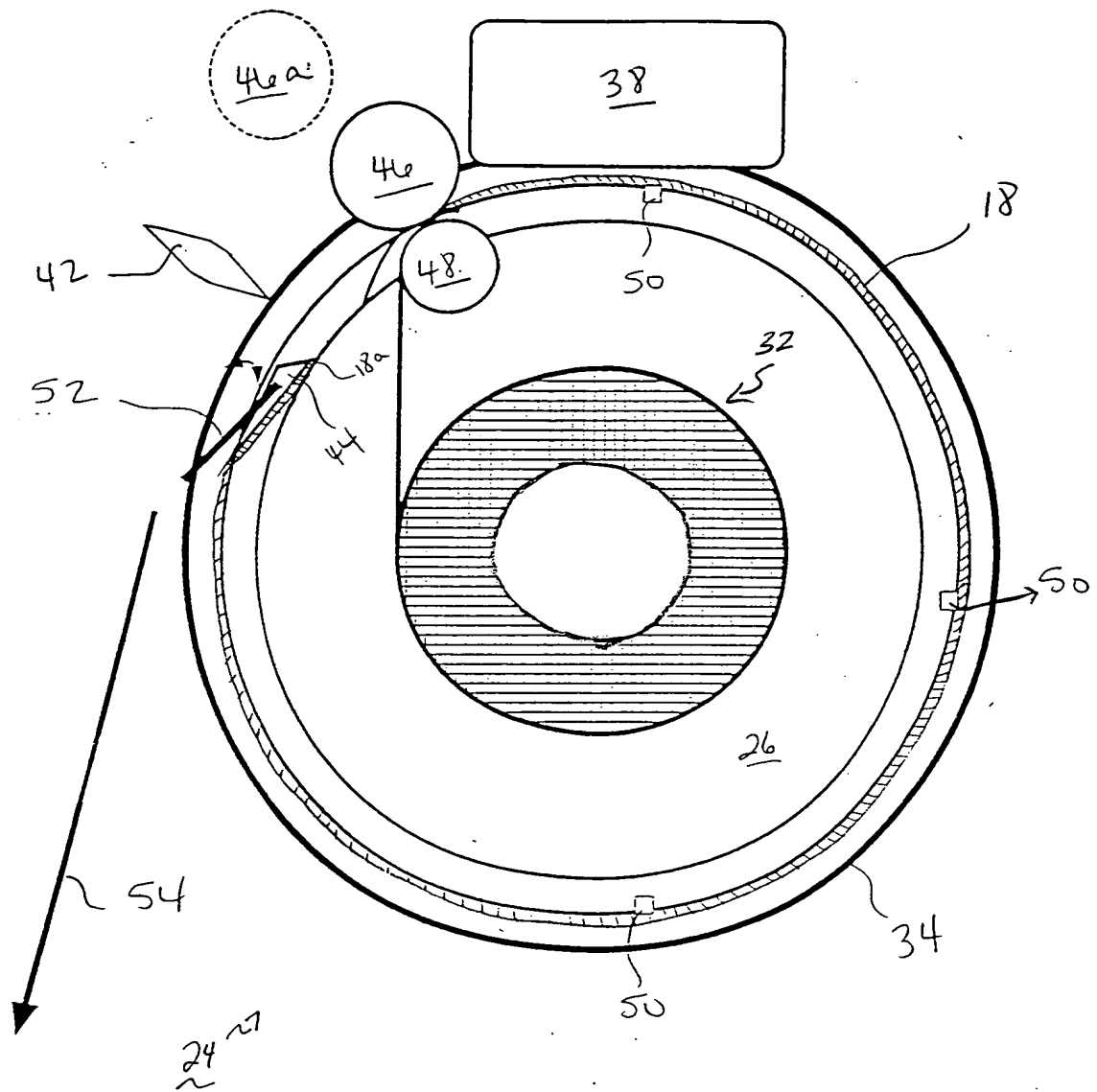


Figure 3C

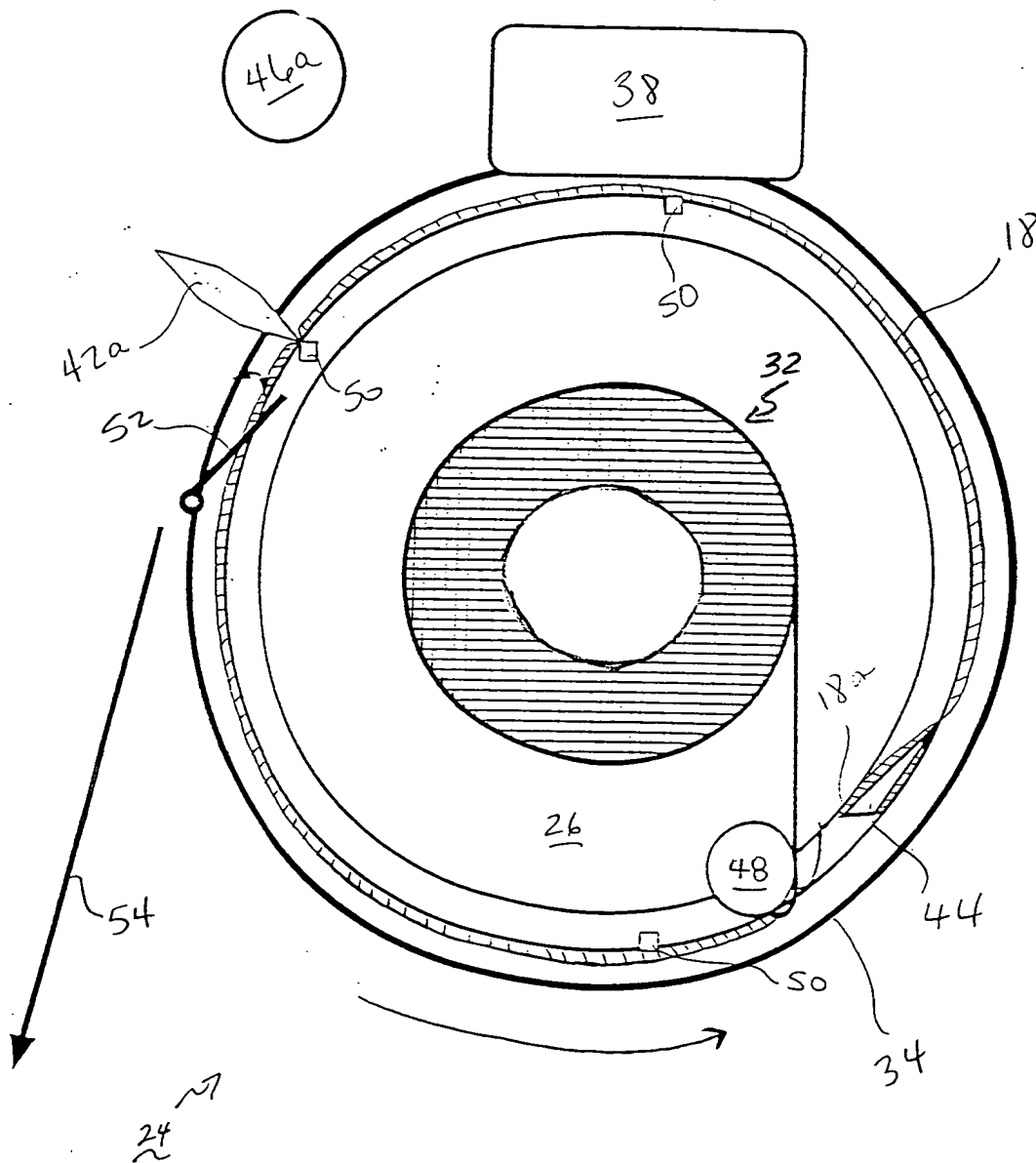


Figure 4A

POST PRINT STATION

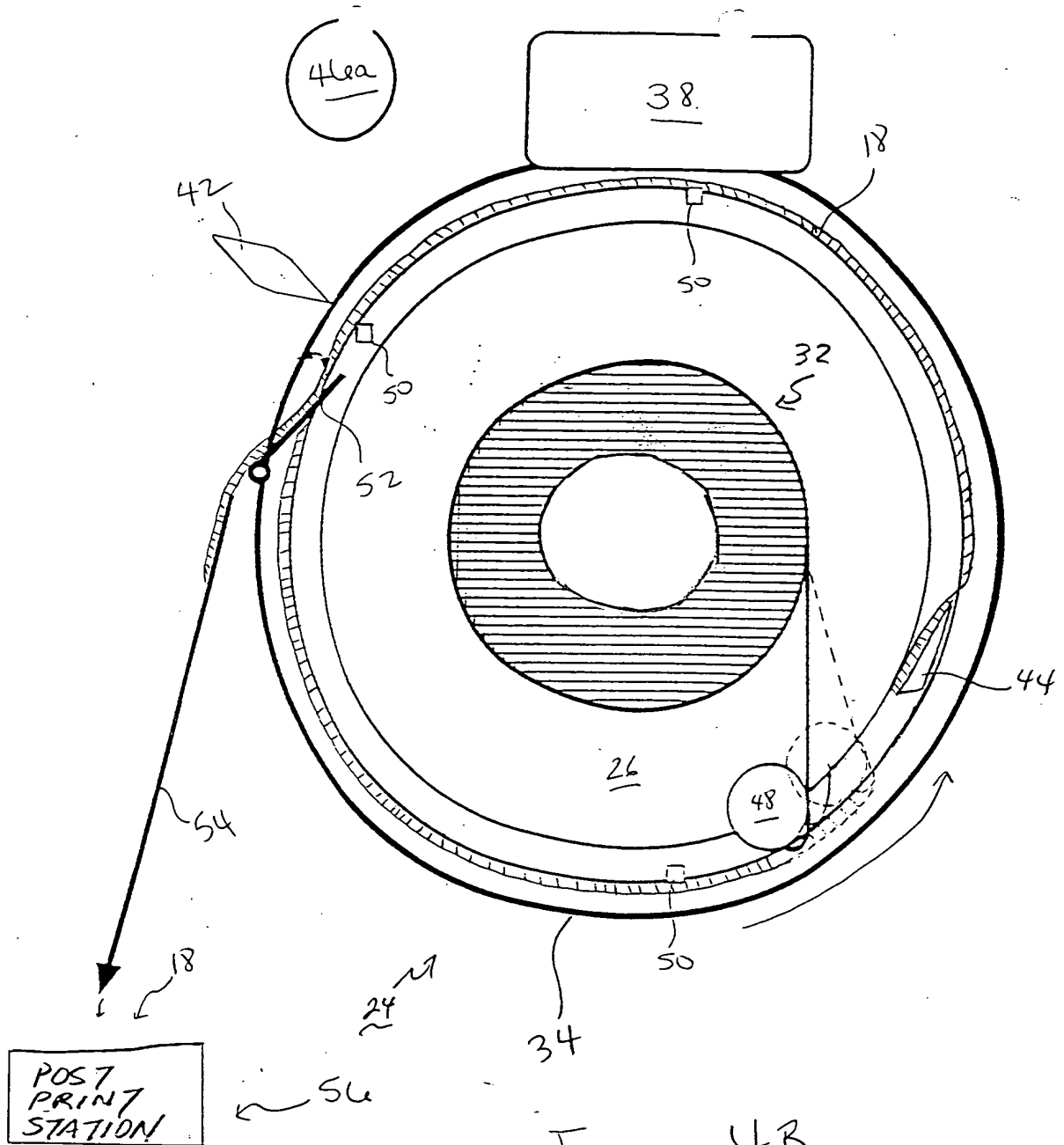


Figure 4B